IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Deane, Peter; Tsay, Chen H.; Wang, Albert

Assignee:

National Semiconductor Corporation

Title:

A Dual-Direction Over-Voltage And Over-Current IC Protection Device

And Its Cell Structure

Serial No.:

Not known

Filing Date:

Not known

Examiner:

Not known

Group Art Unit:

Not known

Docket No.:

NS-3868-1C US

San Jose, California October 23, 2001

BOX PATENT APPLICATION COMMISSIONER FOR PATENTS Washington, D. C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

The present application is a continuation application of U.S. patent application ("Parent Application"), serial no. 09/100,384, filed on June 19, 1998. Please amend the above-referenced patent application as follows:

In the Specification

On page 1, insert at line 7, before the "BACKGROUND OF THE INVENTION" section:

CROSS-REFERENCE TO RELATED PATENT APPLICATIONS

This application is a continuation application of U.S. patent application, serial no. 09/100,384, entitled "A Dual-Direction Over-Voltage And Over-current IC Protection Device and Its Cell Structure," by Wang et al., assigned to Advanced Micro Devices, Inc., which is also the Assignee of the present application.

LAW OFFICES OF SKJERVEN MORRILL MACPHERSON LLP

25 METRO DRIVE SUITE 700 SAN JOSE, CA 95110 (408) 453-9200 FAX (408) 453-7979

Serial No. 09/100,384